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PATENT APPLICATION
Do. No. 5484-092

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Tae-Pok RHEE

Serial No. 09/935,002

Examiner: Tsai, H Jey

Filed: August 21, 2001

Group Art Unit: 2812

For: INDUCTOR FOR SEMICONDUCTOR DEVICE AND METHOD OF MAKING
SAME (as amended)

Commissioner of Patents and Trademarks,
Washington, D.C. 20231

RESPONSE TO ELECTION REQUIREMENT

Responsive to the Election Requirement mailed May 29, 2002, please amend the
application as follows:

IN THE CLAIMS:

Please add the following new claim:

49. (New) A method of making a semiconductor device including an inductor,
comprising the steps of:
forming a groove in an insulating layer on a semiconductor substrate;
forming lower conductive lines across the groove;
forming a cylindrical insulator or a magnetic core above said lower conductive lines and
aligned with said groove; and
forming upper conductive lines over said magnetic core;
electrically coupling said upper conductive lines to said lower conductive lines.

B' sup

Not generic
claim,
containing
mutually
exclusive
species

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TRANSMITTAL LETTER

Assistant Commissioner for Patents
Washington, D.C. 20231

Enclosed for filing in the above-referenced application are the following:

- ☒ Response to Election Requirement
- ☒ Return Post Card

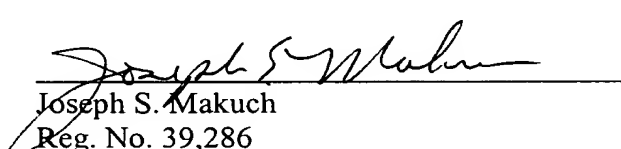


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PATENT TRADEMARK OFFICE

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.


Joseph S. Makuch
Reg. No. 39,286

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1030 SW Morrison Street
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